DESIGN OF SYNTHETIC ELEMENTS WITH TRANSIMPEDANCE OPERATIONAL AMPLIFIERS

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Summary
One way how to design active filters for wider frequency band is application of transimpedance operational amplifiers (TOAs) based on current mode. For this aim the TOAs exhibit the most suitable properties: large bandwidth, high slew rate, good linearity, high gain, low offset voltage, etc. The paper shows their possibilities for creation of synthetic elements. There will be discussed the relevant problems of synthetic elements above their properties with application of real TOAs. Theoretical relations of synthetic elements calculation will be derived on the base of real TOAs equivalent circuit and possibility of the synthetic elements to design and synthesis of low-sensitivity ARC filters will be shown.

1. INTRODUCTION

Synthetic inductors (SI) are used in many circuits instead of real inductors. Merit of this replacement is in minor size, linearity of parameters and higher quality of inductor. For this aim are widely used immitance converters. The generalized immitance converter (GIC) is usually based on known Antoniou’s two Op-Amp circuit (Fig. 1). [1]

Interconnection of input X and output of a real TOA by capacitor is a source of instability; hence this substitution will not further be considered.

2. 

In this case the network represents a synthetic inductor with impedance

$$Z_{\text{id}} = \frac{sR_2 R_5 C_4}{R_2} = sL_{\text{id}}$$ (2)

An important question is: how properties of real TOAs affect properties of synthetic elements. For illustration we bring here results that were obtained from simulation of synthetic inductor circuit with TOAs type AD844 by PSPICE program (the library of PSPICE program contains the real model of this device). Supposed element values are:

$$R_1 = R_2 = R_3 = R_5 = 1 \, \text{k}\Omega, \ C_4 = 1 \, \text{nF}$$

The obtained frequency dependence of absolute value of SI impedance with real TOAs is in Fig. 2.

From Fig. 2 it is evident that SI behaves as a resistor in low frequency range, an inductor in middle frequency range and a parallel resonance circuit in the range of high frequencies. On the base of Fig. 2 we can draw an equivalent scheme for the synthetic inductor shown in Fig. 3. It consists of serial combinations of elements $L$, $R_s$ which are connected in parallel with elements $R_p$ and $C$. 
2. INVESTIGATION OF SI PROPERTIES

The aim of investigation is derivation of formulas for element values of SI equivalent scheme. In the first step the SNAP program was used. It performs the symbolic analysis of circuit functions [2]. SNAP program, however, delivers a very complicated analytic formula of SI impedance, of which it is hard to derive relations for element values of SI equivalent scheme. Analytic expressions describing the effect of real TOAs can be derived only for the simplest cases [3]. At last it was chosen an approximation of PSPICE program numerical results to the relation for element values.

Ideal TOA (Fig. 4) is described by following formulas

\[ I_X = I_Z, \quad I_Y = 0, \quad V_Y = V_X, \quad V_{out} = Z_I X. \quad (4) \]

where \( Z_I \) is transimpedance \( (Z_I \rightarrow \infty) \).

Ideal TOA in PSPICE program can be modelled by controlled voltage and current sources with unity gains [4].

Influence of real TIAs properties on synthetic element parameters will be investigated by the equivalent circuit consisting of ideal TOA and added parts. Additional elements \( R_{Y1}, R_{Y2}, C_{Y1}, C_{Y2}, R_{T1}, R_{T2}, C_{T1}, C_{T2} \) simulate a finite value of input \( Y \) impedance and transimpedance \( Z_I \) of TOA1, TOA2 and their frequency dependencies. Non-zero input \( X \) and amplifier output impedances are modelled by resistors \( R_{X1}, R_{X2}, R_{out1}, R_{out2} \) and capacitor \( C_{X1}, C_{X2} \).

This equivalent circuit is drawn in Fig. 5. Model elements of the first TOA are here marked by index 1 and model elements of the second TOA by index 2. Verification of the SI model in Fig. 5 was performed by application of TOAs catalogue values to the SI model [5]. Results obtained by SI model and models built in the PSPICE program were identical. Because the PSPICE program works on a base of numerical values it is needed to provide restriction in values of si model elements. At further we will be investigating the SI with identical GIC circuit resistors. For this case we introduce a designation:

\[ R_1 = R_2 = R_3 = R_5 = R. \]

In relation to real TOAs parameters derivation of relations will be performed for the following ranges of element values:

\[ 1.10^5 \Omega \leq R_{Y1}, R_{Y2}, R_{X1}, R_{X2} \leq 1.10^7 \Omega, \]
\[ 1\Omega \leq R_{T1}, R_{T2}, R_{out1}, R_{out2} \leq 100 \Omega, \]
\[ 100 \Omega \leq R \leq 10 \Omega, \]
\[ 1 \text{pF} \leq C_{X1}, C_{X2}, C_{T1}, C_{T2} \leq 5 \text{pF}, \]
\[ 2 \text{pF} \leq C_{Y1}, C_{Y2} \leq 10 \text{pF}. \]
3. DETERMINATION THE VALUES OF SI EQUIVALENT CIRCUIT ELEMENTS

For determination of equivalent circuit values of SI scheme will be below in text used this parameters of TOAs:

\[ R_{t1} = R_{t2} = 1.10 \times 10^{-6} \, \Omega, \quad R_{y1} = R_{y2} = 1.10 \times 10^{-7} \, \Omega, \]
\[ R_{x1} = R_{x2} = 50 \, \Omega, \quad R_{\text{out1}} = R_{\text{out2}} = 15 \, \Omega, \]
\[ R = 1 \, k\Omega, \quad C_{t1} = C_{t2} = 5 \, pF, \]
\[ C_{x1} = C_{x2} = C_{y1} = C_{y2} = 2 \, pF. \]

The result of investigation is a high quantum of data. For illustration we introduce here some partial results. In this case the value of capacitor \( C_4 = 100 \, nF \) was chosen. The influence of a finite value of resistor \( R_{t1} \) illustrates Fig. 6. The dependences in Fig. 6 show that the value of resistor \( R_s \) decreases and that of resistor \( R_p \) increases with increasing of resistor \( R_{t1} \).

![Fig. 6 The influence of transresistor value \( R_{t1} \) on frequency dependence of SI impedance](image)

Tab. 1 shows an influence of \( R_{t1} \) resistor values on values of resistors \( R_{t1}, R_{pt1} \). Here the index “t1” indicates the influence of resistor \( R_{t1} \).

<table>
<thead>
<tr>
<th>( R_{t1} ) [M( \Omega )]</th>
<th>0.1</th>
<th>0.3</th>
<th>1</th>
<th>3</th>
<th>10</th>
</tr>
</thead>
<tbody>
<tr>
<td>( R_{xt1} ) [m( \Omega )]</td>
<td>554</td>
<td>220</td>
<td>102</td>
<td>69</td>
<td>57</td>
</tr>
<tr>
<td>( R_{xtt1} ) [k( \Omega )]</td>
<td>84.8</td>
<td>213</td>
<td>451</td>
<td>661</td>
<td>790</td>
</tr>
</tbody>
</table>

On base of total inspection of numerical and graphical results approximate relations for calculation of SI equivalent scheme element values were derived. The resistor \( R_s \) value is dependent on values of resistors \( R, R_{t1}, R_{x1}, R_{\text{out}} \). This dependence can be described by following relation

\[ R_s = \frac{(R_{t1} + R_{t2}) \cdot (R_{y1} + R_{y2})}{2R_{t1} \cdot R_{t2}} \cdot \left[ R + 0.75(R_{\text{out1}} + R_{\text{out2}}) \right]. \] (5)

In described case of the circuit the values resistors in operational network are the same, so we obtain from (3):

\[ L = R^2 \cdot C_4. \] (6)

Now we take into account that the TOA \( R_{t1} \) is connected in parallel with the resistor \( R_{s} \) (Fig. 5). This configuration we replace by resistor

\[ R' = \frac{R_{t1} \cdot R_{t2}}{R + R_{t2}}. \] (7)

The capacitor \( C_{y2} \) is connected in parallel with the resistor \( R \). The resistor \( R \) value is relatively low in comparison with the capacitor \( C_{y2} \) reactance therefore an effect of capacitor \( C_{y2} \) is practically insignificant and the SI inductance can be calculated from the relation

\[ L = R \cdot R' \cdot C_4 = R^2 \cdot R_{t2} \cdot C_4. \] (8)

The non-zero values of resistors \( R_{t1}, R_{t2} \) and the finite value of resistor \( R_{y1} \) create the resistor \( R_{pt} \) which is connected in parallel to the \( L \). The first part of the term (9) presents influence of the resistors \( R_{t1}, R_{t2} \) and the second part shows the influence of the resistors \( R, R_{x1} \) and \( R_{x2} \). From Fig. 5 it is evident that the TOA \( R_{\text{t1}} \) input resistor \( R_{y1} \) creates a parallel circuit with the resistor \( R_{pt1} \). Consequently we can express the \( R_p \) resistor value by relation

\[ R_p = \frac{R_{pt1} \cdot R_{yt1}}{R_{pt1} + R_{yt1}}. \] (9)

An investigation of the influence of TIAs “parasitic” capacitances brings an interesting discovery, which it leads to relations for capacitances

\[ C = C_{t1} + C_{t2} + C_{y1} - C_{x1} - C_{x2}. \] (11)

4. DETERMINATION OF WORKING AREA

Working area of SI starts under condition

\[ R_s = 2 \pi f_d L \] (12)

which is fulfilled on frequency

\[ f_d = R_s / 2 \pi L. \] (13)

For high frequency this working area is limited due parallel resonance. Resonant frequency of the parallel circuit is

\[ f_r = \frac{1}{2 \pi \sqrt{LC}}. \] (14)

It is interesting to note that addition of capacitor \( C_k \) (see Fig. 5) has an effect similar to the increase of
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capacitance $C_{x1}$ or $C_{x2}$. Thus, due to (11) and (14) the resonant frequency can be increased.

This phenomenon will occur for

$$C_k \leq C_{k_{\text{max}}},$$

where

$$C_{k_{\text{max}}} = C_{x1} + C_{x2} + C_{y1} - C_{x1} - C_{x2}.$$  \hfill (16)

Now we will show on example of SI with inductance $L = 0.1 \text{ H}$ the possibility of resonant frequency increasing. In this case $C_{k_{\text{max}}}$ value is:

$$C_{k_{\text{max}}} = C_{x1} + C_{x2} + C_{y1} - C_{x1} - C_{x2} = 8 \text{ pF}.$$  \hfill (17)

Corresponding numerical results of capacitor $C_k$ versus resonant frequency $f_r$ and resistor $R_p$ values are in Tab. 2. Fig. 7 shows the corresponding curves for capacitor $C_k$ values

$$\begin{array}{|c|c|c|}
\hline
C_k \text{ [pF]} & f_r \text{ [kHz]} & R_p \text{ [k\Omega]} \\
\hline
0 & 173.8 & 439.8 \\
4 & 289.9 & 442.4 \\
6 & 327.3 & 447.1 \\
8 & 901.6 & 516.7 \\
10 & 398.1 & 120 \\
12 & 266.1 & 81.7 \\
\hline
\end{array}$$  

Fig. 7 The influence of capacitor $C_k$ value on a resonant frequency

5. DESIGN OF SI

Taking advantage of relations derived above we can design SI with respect to real properties of TOAs. We can include tolerance of parameters into calculation; application of described theory we now show on SI design of inductance $L = 10 \text{ mH}$.

Used TOA AD844 has equivalent circuit element values [5]: $R_X = 50 \text{ \Omega}$, $R_Y = 10 \text{ M\Omega}$, $C_Y = 2 \text{ pF}$, $R_Y = 3 \text{ M\Omega}$, $C_Y = 4.5 \text{ pF}$, $R_{\text{out}} = 15 \text{ \Omega}$.

Let’s choose value of $R = 500 \text{ \Omega}$. Neglecting value of $R_{\text{out}}$ we can write using (8)

$$C_4 = \frac{L}{R^2} = 40 \text{ nF}.$$  

Now we can compute values of remaining parts in schematic Fig. 3 using (5), (10), (11):

$$R = \frac{(R_{x1} + R_{x2}) (R_{x1} + R_{x2})}{2 R_{x1} R_{x2}} \frac{[R + 0.75(R_{\text{out}} + R_{\text{out}})]}{[R + 0.75(R_{\text{out}} + R_{\text{out}})]} = 17.42 \text{ m\Omega},$$

$$R_{\text{pet}} = R_{x1} R_{x2} \frac{R}{R_{x1} + R_{x2} + 0.5(R_{x1} + R_{x2})} = 1.364 \text{ M\Omega},$$

$$R_p = \frac{R_{\text{pet}} R_{y1}}{R_{\text{pet}} + R_{y1}} = 1.364 \text{ M\Omega},$$

$$C = C_{x1} + C_{x2} + C_{y1} - C_{x1} - C_{x2} = 7 \text{ pF}.$$  

Working area of this SI has lower and upper frequency limits (13), (14)

$$f_\text{d} = \frac{R}{2 \pi L} = 0.2772 \text{ Hz},$$

$$f_r = \frac{1}{2 \pi \sqrt{LC}} = 601.5 \text{ kHz}.$$  

6. CONCLUSION

Real TOAs influence the characteristics of synthetic elements. Analytic expressions describing the effect of real TOAs can be derived only for the simplest cases.

In this paper there were derived relations of synthetic inductor calculation on the base of an SI model with real TOAs equivalent circuit. For this aim there was used the program PSPICE v. 9.2, which generated vast amount of data. Gained data were a base for creation of approximate relations, which enabled real SI parameter calculation from TOAs catalogue data. These relations establish simple criteria for optimum selection of TOA type in association with required properties of synthetic inductors.

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REFERENCES


